

AMENDMENTS TO THE SPECIFICATION

Please replace the Abstract of the Disclosure with the new Abstract of the Disclosure appearing on the following page.

ABSTRACT

There is provided a display processor processing device that increases graphics display performance. The display processor includes including: an OSD plane (211) having a first OSD area (671) and a second OSD area (672) for storing graphics images; and a Z-order manager, management unit (601) which stores a vertical order assigned to the first OSD area (671) and the second OSD area (672), and which provides a notification regarding that vertical order. This display processor also includes a CPU, (217) which executes a downloaded application (506), thereby storing the graphics images in the first OSD area (671) and the second OSD area (672) based on the vertical order notified by the Z-order management unit (601); and a display, (212) which superimposes the graphics images stored in the first OSD area (671) and the second OSD area (672) in accordance with the abovementioned vertical order, and displays the images.